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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10821160	
	Filing Date		2004-04-08	
	First Named Inventor	Adrian C. Anderson		
	Art Unit	2138		
	Examiner Name	Saib Javaid Siddiqui		
	Attorney Docket Number	ROC920030422US1		

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SS	1	GUO et al., "A Technique for Fault Diagnosis of Defects in Scan Chains", IEEE, paper 10.2, 2001, pgs. 268-277, USA	<input type="checkbox"/>
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SS	4	5657332		1997-08-12	Auclair et al.		
SS	5	5859804		1999-01-12	Hedberg et al.		
SS	6	6278956	B1	2006-08-21	Leroux et al.		
SS	7	6308290	B1	2001-10-23	Fortenza et al.		
SS	8	6442720	B1	2002-08-27	Koprowski et al.		

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SS	9	6694454	B1	2004-02-17	Stanley	
SS	10	7058869	B2	2006-06-06	Abdel-Hafez et al.	

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SS	1	20020125907	A1	2002-09-12	Kurtulik et al.	

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SS	1	ISMET BAYRAKTAROGLU et al., "Gate Level Fault Diagnosis in Scan-Based BIST," Proceedings of the 2002 Design Automation and Test in Europe Conference, 1530-1591/02 IEEE.	<input type="checkbox"/>

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